

Google

drjatorres@gmail.com | Search History | My Account | Sign out
Web Images Video News Maps more »
"second tapped delay line" summing weighted Advanced Search Preferences

Web Results 1 - 10 of about 59 for "second tapped delay line" summing weighted tapped equalizer. (0.64s)

Tap-weight controller for adaptive matched filter receiver ...

In an adaptive matched filter with a decision feedback **equalizer** (DFE), ... a **tap-weight** controller having a **second tapped delay line** of a series of delay ...
www.freepatentsonline.com/5353306.html - 42k - Cached - Similar pages

Maximum likelihood estimation using reference and spontaneous ...

A **second tapped-delay line** is formed by delay-line taps 11' to receive ... **tap** signals on the delay line, and an adder for **summing the weighted tap signals**. ...
www.freepatentsonline.com/5604724.html - 41k - Cached - Similar pages
[More results from www.freepatentsonline.com]

Fast response matched filter receiver with decision feedback ...

a **second tapped delay line** having a series of delay elements for ... and means for **summing** output signals of said **tap-weight** multipliers to produce a filter ...
www.patentstorm.us/patents/5369668-claims.html - 20k - Cached - Similar pages

Fast response matched filter receiver with decision feedback ...

A **tap-weight** controller includes a **second tapped delay line** having a series of ... its successive **summing** operations when the demodulator is synchronized. ...
www.patentstorm.us/patents/5369668-description.html - 26k - Cached - Similar pages
[More results from www.patentstorm.us]

Equalizer architecture patent invention

An **equalizer** is divided between two **tapped** delay lines. ... An output is then formed as the **weighted sum** of sample values at the sequence of **tap** points. ...
www.freshpatents.com/Equalizer-architecture-dt20050915ptan20050201455.php - 26k - Supplemental Result - Cached - Similar pages

Equalizer architecture patent invention

An **equalizer** is divided between two **tapped** delay lines. ... a first **summing** circuit, for forming a first output as a **weighted sum** of sample values from a ...
www.freshpatents.com/Equalizer-architecture-dt20050915ptan20050201455.php?type=claims - 28k - Supplemental Result - Cached - Similar pages
[More results from www.freshpatents.com]

Tap-weight controller for adaptive matched filter receiver ...

a **tap-weight** controller having a **second tapped delay line** of a series of delay ... averaging means for successively **summing** the digital sample from the ...
www.wipatents.com/5353306.html - 127k - Cached - Similar pages

Quadriphase shift keyed adaptive equalizer - Patent Review 4053837

The adaptive **equalizer** filter is generally configured in the form of **tapped** delay line transversal filters with each **tap** being **weighted** and the taps being ...
www.wipatents.com/4053837.html - 127k - Cached - Similar pages
[More results from www.wipatents.com]

Phase-directed decision feedback equalizer

The output of decision logic 37 is applied to an output line 20 as well as to the input of a **second tapped delay line** 38 identical to delay line 33. ...
www.emergenthomestore.com/techcat/satellite_retrieval/phase-

[directed_decision_feedback_equalizer.html](#) - 105k - Supplemental Result -
[Cached](#) - [Similar pages](#)

[Arbitrary waveform modem - Patent # 6396801 - PatentGenius](#)

By controlling the **tap weight** accuracy, the system in accordance with the ... For example, the **weight** values for an **equalizer** or filter can be stored in the ...
[www.patentgenius.com/patent/6396801.html](#) - 69k - Supplemental Result -
[Cached](#) - [Similar pages](#)

Result Page: [1](#) [2](#) **[Next](#)**

Download [Google Pack](#): free essential software for your PC

[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied? Help us improve](#)

[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [About Google](#)

©2007 Google

About Us

Newsroom

Advisory Board

Submit Web Site

Help

Contact Us

Basic Search

[Advanced Search](#) [Search Preferences](#)

"second tapped delay line" AND summing AND weighte

Journal sources Preferred Web sources Other Web sources Exact phrase

Searched for:: :All of the words:**"second tapped delay line"** AND summing AND weighted AND tapped AN

Found:: :**11 total | 0 journal results | 11 preferred web results | 0 other web results**

Sort by:: :**relevance | date**

Re

us

fo

de

1. **Fast response matched filter receiver with decision feedback equalizer**

Yamamoto, Takeshi, UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, Nov 1994

...having a **tapped** delay line...means for **summing** output signals...feedback **equalizer**. **de**
A tap-weight...includes a **second tapped delay line** having a...feedback **equalizer**; **de**
means for...having a **tapped** delay line...means for **summing** output
signals...**equalizer**; a **second tapped delay line** having a...feedback **equalizer**; a **tra**
transversal...having a **tapped** delay line...means for **summing** output
signals...**equalizer**; a **second tapped delay line** having a... **Or**

Full text available at patent office. For more in-depth searching go to  LexisNexis

[view all 11 results from Patent Offices](#)

[similar results](#)

F

2. **Quadriphase shift keyed adaptive equalizer**

Ryan, Carl R. / Stilwell, James H., UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, Oct 1977

...first and second **tapped** delay lines connected...components and independent **equalizer** coefficient generator...each channel. 2. The **equalizer** according to claim...first and second **summing** means each having...third and fourth **equalizer** coefficient generators...signal from said first **tapped** delay line and the...signal from said **second tapped delay line**; the second inputs... **AI**

Full text available at patent office. For more in-depth searching go to  LexisNexis

[view all 11 results from Patent Offices](#)

[similar results](#)

F

3. **Maximum likelihood estimation using reference and spontaneous output peaks of partial response equalizer**

Shiokawa, Masato, EUROPEAN PATENT APPLICATION, Mar 1996

...comparison with a prior art **equalizer**; and Fig. 11 shows eye...generally comprises an adaptive **equalizer 1**, a maximum likelihood...in Fig. 2, the adaptive **equalizer 1** includes a **tapped-delay line filter formed...of correlators 12**. The **weighted** tap signals are summed...magnetic recording medium. A **second tapped-delay line** is formed by delay-line... **AI**

Full text available at patent office. For more in-depth searching go to  LexisNexis
[view all 11 results from Patent Offices](#)

similar results

4. Arbitrary waveform modem

Upton, Eric L. / Wickham, Michael G., UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, May 2002

...As known in the art, such **tapped** delay lines, for example, formed...said filter architectures, the **tapped** delay lines are known to be...generate a modulated signal, **summing** each of the modulated signals...example, the weight values for an **equalizer** or filter can be stored in...

Full text available at patent office. For more in-depth searching go to  LexisNexis

view all 11 results from Patent Offices

similar results

5. Matched filter for use with a DFE

Yamamoto, Takeshi, EUROPEAN PATENT, Mar 1993

...means for **summing** output signals...feedback **equalizer**. A tap-weight...a **second tapped delay line**...tap of the **second tapped delay line** to produce...feedback **equalizer** (7), wherein...includes a first **tapped** delay line...21) for **summing** output signals...signal of said **equalizer** (7), wherein...includes a **second tapped delay line** (22-26) for...tap of said **second tapped delay line** (22-26) to...

Full text available at patent office. For more in-depth searching go to  LexisNexis

view all 11 results from Patent Offices

similar results

6. DS/CDMA receiver for high-speed fading environment

Ushirokawa, Akihisa / Yoshida, Shosei, UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, Jul 1997

...transversal **equalizer** comprising a **tapped** delay line formed by a series...tap-weight coefficients. The **weighted** signals are summed by an...additionally includes a feedback **equalizer** as illustrated in FIG. 3...multiplier 31 where it is **weighted** by a tap weight coefficient b.sub.i (k). After **summing** the **weighted** tap signals by an adder...adders 22 and 32 of the **equalizers** is taken by a subtractor...

Full text available at patent office. For more in-depth searching go to  LexisNexis

view all 11 results from Patent Offices

similar results

7. Tap-weight controller for adaptive matched filter receiver

Yamamoto, Takeshi, UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, Oct 1994

...with a decision feedback **equalizer** according to the present...to a decision feedback **equalizer** 7 which is clocked at...Transversal filter 8 includes a **tapped** delay line formed by a...controller 9 to produce **weighted** digital signals. The outputs...transversal filter has a first **tapped** delay line of a series...which is supplied to an **equalizer**, such as decision feedback...tap-weight controller has a **second tapped delay line** for receiving the digital...

Full text available at patent office. For more in-depth searching go to  LexisNexis

view all 11 results from Patent Offices

similar results

8. Maximal correlation symbol estimation demodulator

Cui, Jian / Hilborn, Derek Stephen, UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, Aug 2000

...signal is then fed into a **tapped** delay line implemented...of K delay blocks 18. A **second tapped delay line** 19 identical in structure...the art that F.sub.R is a **weighted** average of previous correlations...simultaneously, to serve as an **equalizer**, or to provide an enhanced...signal in a conventional **equalizer** or diversity combiner...diversity combining or **equalizer** implementations that use...

Full text available at patent office. For more in-depth searching go to  LexisNexis

view all 11 results from Patent Offices

similar results

9. Parallel transmission of TDMA sync and data bursts by artificial frequency selective fade and decision feedback equalization

Tsujimoto, Ichiro, *EUROPEAN PATENT*, Mar 1994

...bursts and the use of a decision feedback **equalizer** at the receive site for extracting the...a spectral null. A decision feedback **equalizer** is connected to the synchronous detector...modulator. Preferably, the decision feedback **equalizer** comprises a feedforward filter comprising...

Full text available at patent office. For more in-depth searching go to  LexisNexis

view all 11 results from Patent Offices

similar results

10. Fir filter apparatus for multiplexed processing of time division multiplexed signals

Christopher, Lauren A., *UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Feb 1995

...In an embodiment of this type, the **equalizer** and deghoster may be provided a reference...filters. Since such a deghoster and/or **equalizer** are trained with reference to a time...and applied to independent parallel **equalizer** and deghoster circuits, before being...circuitry. FIG. 8 illustrates an output **weighted** FIR filter for processing time division...are coupled to common weighting and **summing** structure. Time division multiplexed...

Full text available at patent office. For more in-depth searching go to  LexisNexis

view all 11 results from Patent Offices

similar results

11. FIR filter apparatus for processing of time division multiplexed signals

Christopher, Lauren Ann, *EUROPEAN PATENT*, Feb 1994

...In an embodiment of this type, the **equalizer** and deghoster may be provided a reference...filters. Since such a deghoster and/or **equalizer** are trained with reference to a time...and applied to independent parallel **equalizer** and deghoster circuits, before being...display. FIGURE 8 illustrates an output **weighted** FIR filter for processing time division...

Full text available at patent office. For more in-depth searching go to  LexisNexis

view all 11 results from Patent Offices

similar results

Sponsored links

Tapped Delay Line

3V & 5V, Active, Surface-Mount 70% Smaller Than the

Competition

www.maxim-ic.com



[Downloads](#) | [Subscribe to News Updates](#) | [User Feedback](#) | [Advertising](#)

[Tell A Friend](#) | [Terms Of Service](#) | [Privacy Policy](#) | [Legal](#)

[Powered by FAST](#) © Elsevier 2007

[Home](#) | [Login](#) | [Logout](#) | [Access Information](#) | [Alerts](#) |

Welcome United States Patent and Trademark Office

 Search Results[BROWSE](#)[SEARCH](#)[IEEE XPLORE GUIDE](#)

Results for "(second tapped delay line<in>metadata) <and> (pyr >= 1950 <and> pyr <=...)"

[e-mail](#)

Your search matched 2 of 1532162 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by **Relevance** in **Descending** order.» **Search Options**[View Session History](#)[Modify Search](#)[New Search](#)

((second tapped delay line<in>metadata) <and> (pyr >= 1950 <and> pyr <= 2004))

[Search](#) Check to search only within this results set» **Key**Display Format: Citation Citation & Abstract**IEEE JNL** IEEE Journal or Magazine[view selected items](#) [Select All](#) [Deselect All](#)**IET JNL** IET Journal or Magazine**IEEE CNF** IEEE Conference Proceeding**IET CNF** IET Conference Proceeding**IEEE STD** IEEE Standard1. **Wide-band adaptive antenna nulling using tapped delay lines**

Mayhan, J.; Simmons, A.; Cummings, W.;
Antennas and Propagation, IEEE Transactions on [legacy, pre - 1988]
Volume 29, Issue 6, Nov 1981 Page(s):923 - 936

[AbstractPlus](#) | [Full Text: PDF\(1384 KB\)](#) [IEEE JNL](#)
[Rights and Permissions](#)2. **Field-programmable-gate-array-based time-to-digital converter with 200-p**

Kalisz, J.; Szplet, R.; Pasierbinski, J.; Poniecki, A.;
Instrumentation and Measurement, IEEE Transactions on
Volume 46, Issue 1, Feb. 1997 Page(s):51 - 55
Digital Object Identifier 10.1109/19.552156

[AbstractPlus](#) | [References](#) | [Full Text: PDF\(104 KB\)](#) [IEEE JNL](#)
[Rights and Permissions](#)Indexed by
 Inspec®[Help](#) [Contact Us](#) [Privacy &](#)

© Copyright 2006 IEEE -

 PALM INTRANETDay : Tuesday
Date: 4/3/2007
Time: 10:51:16

Continuity Information for 10/817206

Parent Data

No Parent Data

Child Data

No Child Data

[Appln Info](#)[Contents](#)[Petition Info](#)[Atty/Agent Info](#)[Continuity/Reexam](#)[Foreign Data](#)Search Another: Application# or Patent# PCT / / or PG PUBS # Attorney Docket # Bar Code #

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | Home page

 PALM INTRANETDay : Tuesday
Date: 4/3/2007
Time: 10:51:24

Foreign Information for 10/817206

Priority#	Date	Country
0405766.7	03/15/2004	UNITED KINGDOM

[Appln Info](#) [Contents](#) [Petition Info](#) [Atty/Agent Info](#) [Continuity/Reexam](#) [Foreign Data](#)

Search Another: Application# or Patent#
PCT / or PG PUBS #
Attorney Docket #
Bar Code #

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | Home page



PALM INTRANET

Day : Tuesday
Date: 4/3/2007
Time: 10:51:34

Inventor Information for 10/817206

Inventor Name	City	State/Country
WILSON, PAUL	LINLITHGOW	UNITED KINGDOM

[Appln Info](#)[Contents](#)[Petition Info](#)[Atty/Agent Info](#)[Continuity/Reexam](#)[Foreign](#)**Search Another: Application#****or Patent#**

PCT /

or PG PUBS #**Attorney Docket #****Bar Code #**

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)



PALM INTRANET

Day : Tuesday
Date: 4/3/2007
Time: 10:52:54

Correspondence Address for 10/817206

Customer Number	Contact Information	Address
00959 Delivery Mode: PAPER	Telephone: (617)227-7400 Fax: (617)742-4214 E-Mail: No E-Mail Address	LAHIVE & COCKFIELD, LLP ONE POST OFFICE SQUARE BOSTON MA 02109-2127

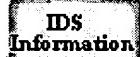
[Appln Info](#)[Contents](#)[Petition Info](#)[Atty/Agent Info](#)[Continuity/Reexam](#)[Foreign](#)**Search Another: Application#****or Patent#****PCT /****or PG PUBS #****Attorney Docket #****Bar Code #**

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | Home page

 PALM IntranetApplication
Number

IDS Flag Clearance for Application 10817206

 IDS
Information

Content	Mailroom Date	Entry Number	IDS Review	Last Modified	Reviewer
M844	2006-07-27	18	N <input type="checkbox"/>	2006-08-03 09:31:18.0	vtowler
M844	2004-09-07	14	N <input type="checkbox"/>	2005-08-10 23:31:48.0	IDS CONV

EAST Search History

L10	2519	375/232	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 10:50
L11	15	8 and 9	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 10:50
L12	26	8 and 10	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 10:50
L13	22	(second near (tap\$3 adj delay adj line)) and summ\$3 and weigh\$3 and tap and equaliz\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 11:11
L14	165	9 and "133"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 11:07
L15	22	13	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 11:07
L16	0	9 and 13	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 11:08

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	1	"10/817206"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 11:18
L3	2	"20040013191".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 10:10
L4	4	("4053837" "4504958").PN.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 10:11
L5	26	"379375"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 10:46
L6	51	(second with (tap\$3 adj delay adj line)) and summing and weigh\$3 and tap and equaliz\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 10:48
L8	92	(second with (tap\$3 adj delay adj line)) and summ\$3 and weigh\$3 and tap and equaliz\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 11:03
L9	2040	375/229	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 10:49

EAST Search History

L17	5	10 and 13	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 11:08
L18	9	(first adj clock) with (second adj clock) with inverse and equaliz\$5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 11:15
L19	2	"5353306".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 11:15
L20	2	"5604724".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 11:16
L21	2	"5604724".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 11:16
L22	2	"5369668".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 11:16
L23	2	"4053837".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 11:20

EAST Search History

L24	2	"5,392,230".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 11:20
-----	---	-----------------	--	----	----	------------------